

In the Claims:

Please amend claims 1 and 3 as follows:

1. (Currently Amended) A multiple-processor information processing system including a plurality of processor modules,

the information processing system comprising:

virtual IP address definition means for defining virtual IP addresses on a ~~process module by processor module basis~~ processor module or a plurality of processor modules;

storage means for storing said virtual IP addresses defined by said virtual IP address definition means and information indicative of ~~one~~at least said real IP address of said processor modules corresponding to said virtual IP addresses, respectively, in a state correlated with each other; and

notification means for notifying a router of asaid virtual IP address of each processor module and asaid real IP address of said each process module as routing information, for said each processor module having said virtual IP address stored in said storage means.

2. (Original) A multiple-processor information processing system according to claim 1, wherein said notification means notifies said router of a destination IP address of each processor module and a real IP address of said each processor module as

routing information, for said each processor module having no corresponding virtual IP address stored in said storage means.

3. (Currently Amended) A recording medium readable by a multiple-processor information processing system including a plurality of processor modules, said recording medium storing a program for causing said multiple-processor information processing system to carry out a process for notification of routing information,

    said program causing multiple-processor information processing system to function as:

    virtual IP address definition means for defining virtual IP addresses on a ~~process module by processor module basis~~ processor module or a plurality of processor modules;

    storage means for storing said virtual IP addresses defined by said virtual IP address definition means and information indicative of ~~one~~at least said real IP address of said processor modules corresponding to said virtual IP addresses, respectively, in a state correlated with each other; and

    notification means for notifying a router of asaid virtual IP address of each processor module and asaid real IP address of said each process module as routing information, for said each processor module having said virtual IP address stored in said storage means.